

2FHD0620 Data Sheet

Abstract

The 2FHD0620 is a high-performance, dual-channel SiC gate driver developed by Firststack, supports SiC modules up to 1700V. The 2FHD0620 can be used individually or in parallel operation within slave boards connected by a set of cables.

Highlights:

- Support max. 4 in parallel
- Support up to 1700V SiC module
- Distributed NTC sampling
- Short-circuit protection(soft shut down)
- Miller clamping
- Intelligent fault feedback
- UVLO

Applications:

- ESS
- Rail
- Motor drives



Fig.1 2FHD0620

Functional Block Diagram

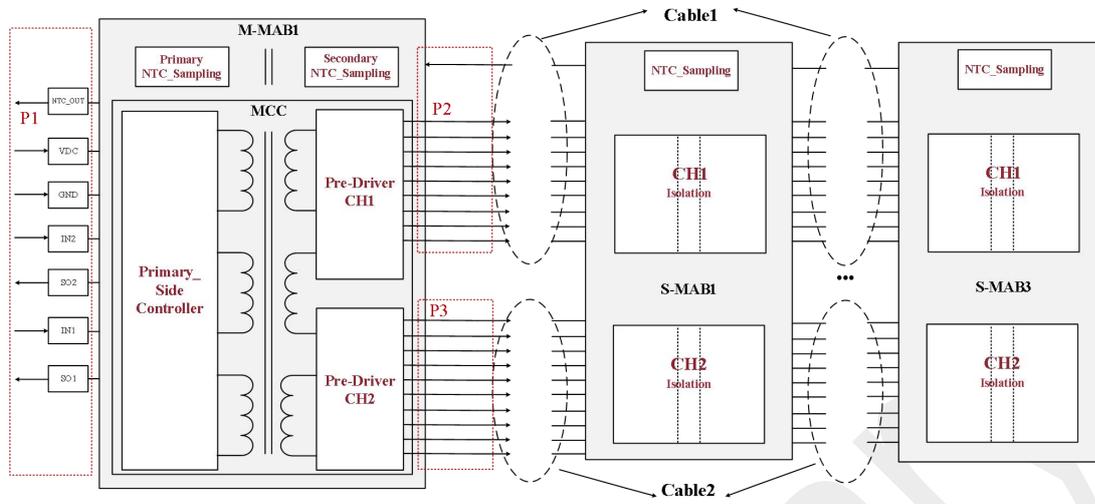


Fig.2 Functional block diagram

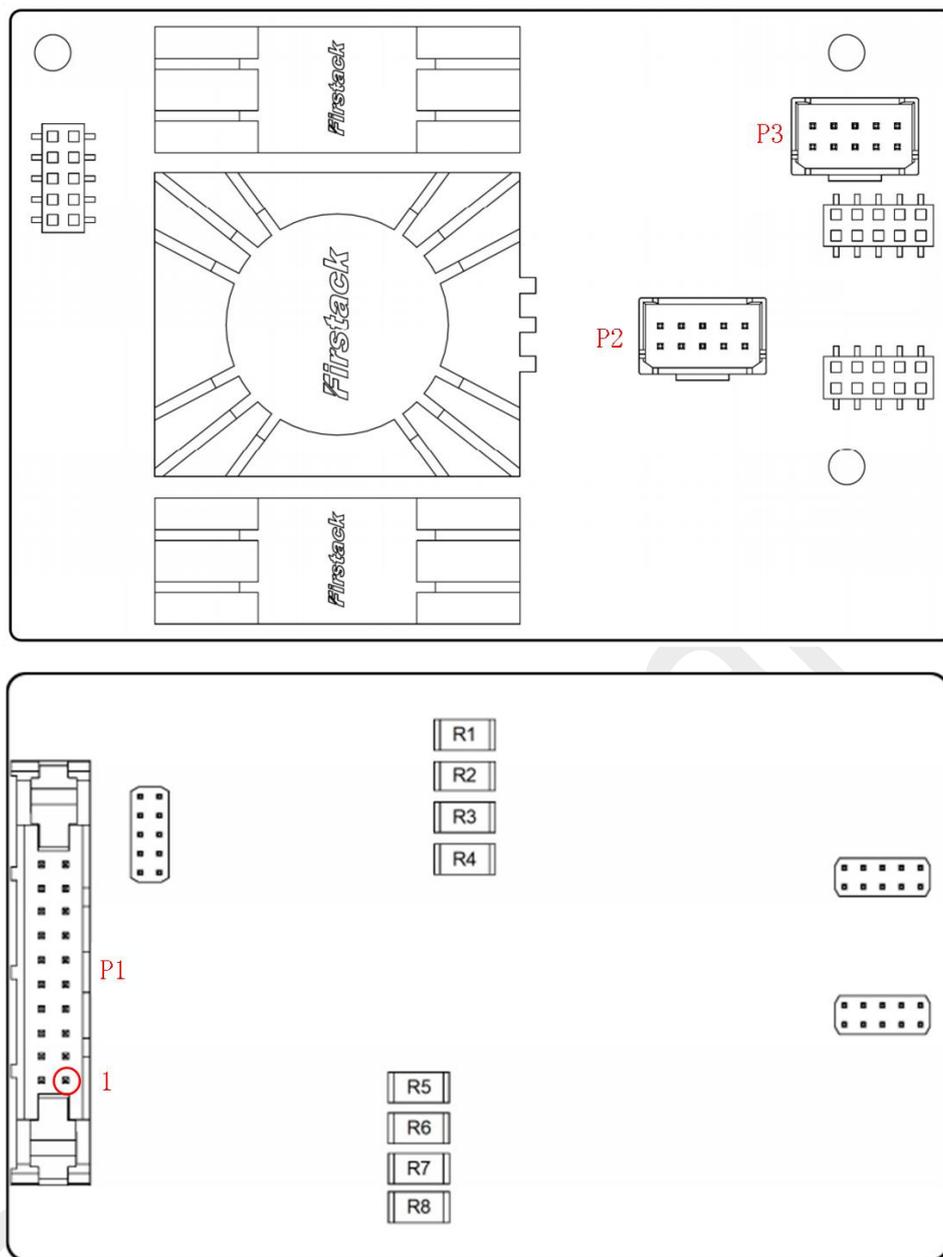
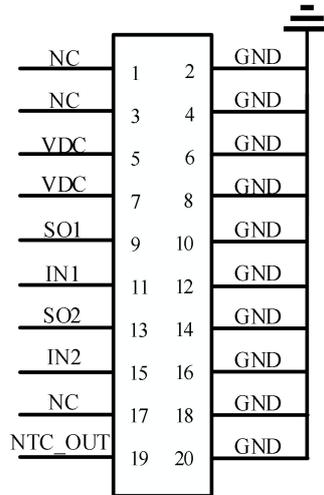


Fig.3 Connector interface location

Resistors Replacement Instructions

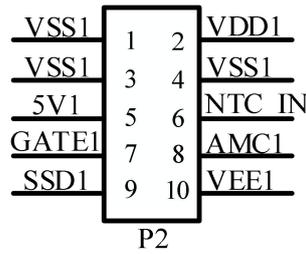
Resistors	Definition
R1,R2 & R7,R8	Turn-on resistor
R3,R4 & R5,R6	Turn-off resistor

Connector Interface Definition



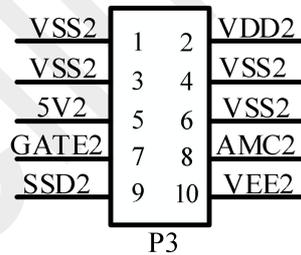
P1 terminal pin designation

Pin	Definition	Function	Pin	Definition	Function
1	NC	Free	2	GND	Primary side ground
3	NC	Free	4	GND	Primary side ground
5	V _{DC}	Power supply input	6	GND	Primary side ground
7	V _{DC}	Power supply input	8	GND	Primary side ground
9	SO1	Status output channel 1	10	GND	Primary side ground
11	IN1	Signal input channel 1	12	GND	Primary side ground
13	SO2	Status output channel 2	14	GND	Primary side ground
15	IN2	Signal input channel 2	16	GND	Primary side ground
17	NC	Free	18	GND	Primary side ground
19	NTC_OUT	NTC sampling	20	GND	Primary side ground



P2 terminal pin designation

Pin	Definition	Function	Pin	Definition	Function
1	VSS1	Secondary side negative voltage channel 1	2	VDD1	Secondary side positive supply channel 1
3	VSS1	Secondary side negative voltage channel 1	4	VSS1	Secondary side negative voltage channel 1
5	5V1	5V supply channel 1	6	NTC_IN	NTC sampling signal channel 1
7	GATE1	Gate signal channel 1	8	AMC1	Miller clamping signal channel 1
9	SSD1	SSD signal channel 1	10	VEE1	Secondary side ground channel 1



P3 terminal pin definition

Pin	Definition	Function	Pin	Definition	Function
1	VSS2	Secondary side negative voltage channel 2	2	VDD2	Secondary side positive supply channel 2
3	VSS2	Secondary side negative voltage channel 2	4	VSS2	Secondary side negative voltage channel 2
5	5V2	5V supply channel 2	6	VSS2	Secondary side negative voltage channel 2
7	GATE2	Gate signal channel 2	8	AMC2	Miller clamping signal channel 2
9	SSD2	SSD signal channel 2	10	VEE2	Secondary side ground channel 2

Technical Parameters

Absolute Maximum Ratings

Parameter	Remarks	Min	Max	Unit
Supply voltage V_{DC}	V_{DC} to GND	0	15.5	V
Gate peak current	@85°C	-15	20	A
Source power per channel	@85°C		6	W
Output power per channel	@85°C		1	W
Test voltage (50Hz/1min)	Primary to secondary side	5000		V_{RMS}
Operating temperature		-40	85	°C
Storage temperature		-40	85	°C

Recommended Operating Conditions

Parameter	Remarks	Min	Typ	Max	Unit
Supply voltage V_{DC}	V_{DC} to GND	14.5	15	15.5	V
Supply current I_{DC}	Without load		0.12		A
Coupling capacitor C_{IO}	Primary to secondary side		/		pF
Primary side undervoltage threshold	Supply voltage		12		V
Secondary side undervoltage threshold	Secondary side positive voltage		12.5		V

Gate Driver Parameters

Output voltage	Remarks	Min	Typ	Max	Unit
V_{GSon}	Turn on (ON)	14.5	18	19.5	V
V_{GSoff}	Turn off (OFF)	-9.5	-4	-0.5	V

Logic Inputs & Outputs

Parameter	Remarks	Min	Typ	Max	Unit
Input signal INx	INx to GND	14.5	15	15.5	V
Input impedance			100		kΩ
Turn-on threshold	V(INx)	9.2			V
Turn-off threshold	V(INx)			3.2	V
Fault output SOx (Note 1)	Protection state @Io<10mA	0.35		V _{DC}	V
MOD mode	Direct mode	Set by software, no configuration required			
	Half-bridge mode	Set by software, no configuration required			

Short-circuit protection

Parameter	Remarks	Min	Typ	Max	Unit
V _{DS} monitoring threshold	Short-circuit protection monitoring	Configurable	11	Configurable	V
Response time	CH1, Note 2	Configurable	1.2	Configurable	μs
	CH2, Note 2	Configurable	1.2	Configurable	μs
Soft shut down time	Soft shut down action time	Configurable	6.24	Configurable	μs

Miller Clamping

Parameter	Remarks	Min	Typ	Max	Unit
Time from drive signal turn-off to clamp turn-on		Configurable	500	Configurable	ns
Time from clamp turn-off to drive signal turn-on			500		ns
Clamp voltage			V _{GSoff}		

Timing Characteristics

Parameter	Remarks	Min	Typ	Max	Unit
Turn-on delay	Note 3		1.2		μs

Turn-off delay	Note 4	1.3	μs
Rise time	Note 5	12	ns
Fall time	Note 6	12	ns
Fault blocking time		80	ms
Fault return time	Note 7	10	ms

NTC Sampling

Parameter	Remarks	Duty cycle for temperature	Unit
Temperature output	Fixed frequency 4K, variable duty cycle, output the highest temperature	Note 8	μs

Electrical Isolation

Parameter	Remarks	Min	Typ	Max	Unit
Creepage distance	Primary to secondary side, Note 9		15		mm
	Secondary to secondary side, Note 9		12.5		mm
Clearance distance	Primary to secondary side		8		mm
	Secondary to secondary side		6		mm

Unless otherwise specified, all data are based on tests at +25°C ambient temperature and V_{DC}=15V.

Note:

1. SO_x provides high level during normal operation and low level within fault. The high level is pulled up to V_{DC}, low level is around 0V;
2. Response time: the time from the occurrence of the fault to the start of soft shut down;
3. Turn-on delay: the time required to transmit the rising edge of the PWM signal input from the primary side to the rising edge of the secondary side of the gate driver;
4. Turn-off delay: the time required to transmit the falling edge of the PWM signal input from the primary side to the falling edge of the secondary side of the gate driver;

5. Rise time: the amount of time from 10% of the gate turn-off voltage to 90% of the gate turn-on voltage;
6. Fall time: the amount of time from 90% of the gate turn-on voltage to 10% of the gate turn-off voltage;
7. Fault return time: short-circuit 10ms, secondary side undervoltage 20ms, primary side undervoltage 40ms;
8. Temperature output duty cycle: (refer to Infineon NTC)

Temperature(°C)	Rntc(kΩ)	Duty Cycle (%)
-40	99.092	6.0%
-35	75.144	8.0%
-30	57.533	10.0%
-25	44.448	12.0%
-20	34.610	14.0%
-15	27.156	16.0%
-10	21.483	18.0%
-5	17.120	20.0%
0	13.727	22.0%
5	11.082	24.0%
10	9.003	26.0%
15	7.359	28.0%
20	6.049	30.0%
25	5.000	32.0%
30	4.156	34.0%
35	3.472	36.0%
40	2.914	38.0%
45	2.458	40.0%
50	2.083	42.0%
55	1.773	44.0%

60	1.515	46.0%
65	1.300	48.0%
70	1.120	50.0%
75	0.968	52.0%
80	0.840	54.0%
85	0.732	56.0%
90	0.640	58.0%
95	0.561	60.0%
100	0.493	62.0%
105	0.435	64.0%
110	0.385	66.0%
115	0.342	68.0%
120	0.304	70.0%
125	0.271	72.0%
130	0.243	74.0%
135	0.217	76.0%
140	0.195	78.0%
145	0.176	80.0%
150	0.158	82.0%

9. Creepage distance: refer to IEC61800-5-1-2007, and meet the basic isolation requirements of below 2km altitude and pollution level 2; this value is the creepage distance of the isolation device.

3D and Mechanical Dimensions

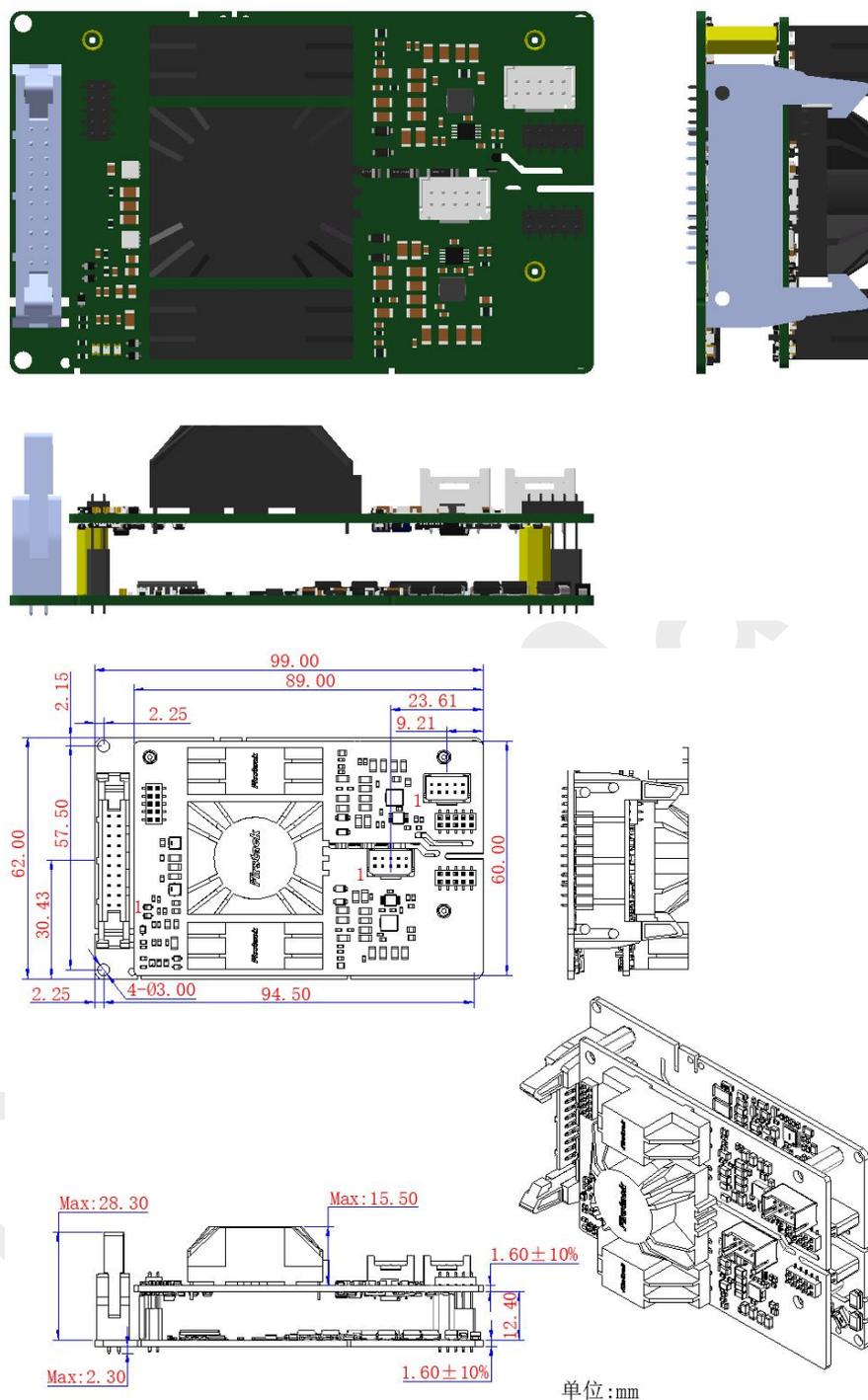


Fig.4 3D and mechanical dimensions (unit: mm)

Note: 1. The thickness tolerance of the board is $\pm 10\%$;

2. Other dimensional tolerances refer to GB/T1804-m.

Updates

Date	Description	Version
2025.02.11	Official version	V1.0
2025.04.23	Ordering information update	V1.1
2025.11.03	Ordering information update	V1.2
2026.02.03	Update of the description of SOx and company logo	V1.3

Ordering Information

2FHD0620 can support different models of package modules from multiple manufacturers. When purchasing, please add the module model number after the driver model number so that we can provide the driver that best meets your needs.

Part Number	R _{GON} (Ω)	R _{GOFF} (Ω)	Gate Voltage	Coating	SOx
2FHD0620M17C1-S0101	4.7/2	4.7/2	18V/-4V	W/O	15V
2FHD0620M17C2-S0101	4.7/2	4.7/2	15V/-5V	W/O	15V
2FHD0620M17C3-S0101	4.7/2	4.7/2	18V/-2V	W/O	15V

Technical Support

Firstack's professional team will provide you with business consultation, technical support, product selection, price, lead time and other related information, and guarantee to answer your questions within 48 hours.

Legal Disclaimer

The instruction manual provides a detailed description of the product but does not commit to providing specific parameters regarding the delivery, performance, or applicability of the product. This document does not offer any express or implied warranties or guarantees.

Firstack reserves the right to modify technical data and product specifications at any time without prior notice. The general delivery terms and conditions of Firstack apply.

Contact Information

Tel: +86-571 8817 2737

Fax: +86-571 8817 3973

Website: www.firstack.com

Email: fsales@firstack.com

Address: 4-5/F, Building/5, Xizi Wisdom Industrial Park, No.1279 Tongxie Road, Hangzhou, China