

2FHD0320V Data Sheet

Abstract

The 2FHD0320V is a high-performance, dual-channel plug-and-play gate driver with optical interface developed by Firstack based on intelligent chip technology for PrimePACK™ packages, it supports IGBT modules up to 1700V. It is a plug-and-play device that can drive IGBT modules safely and reliably without other peripheral circuits.

Highlights:

- 3W/20A, support up to 50kHz applications
- Support up to 1700V module
- Short-circuit protection (soft shut down)
- Digital control method

Applications:

- Motor drives
- ESS

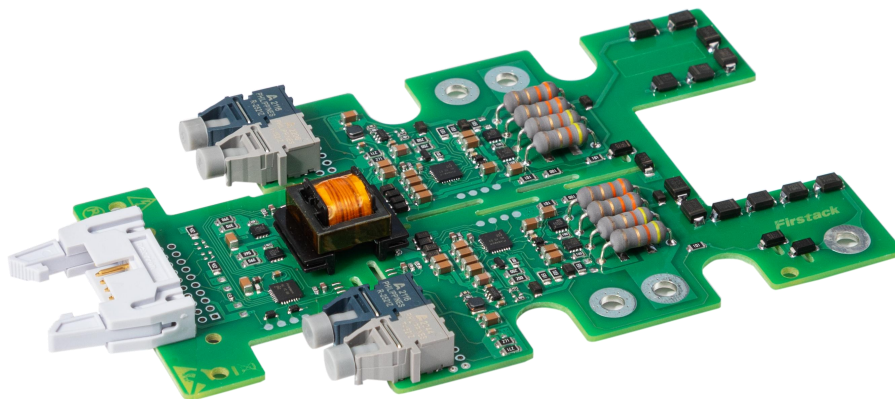


Fig.1 2FHD0320V

Functional Block Diagram

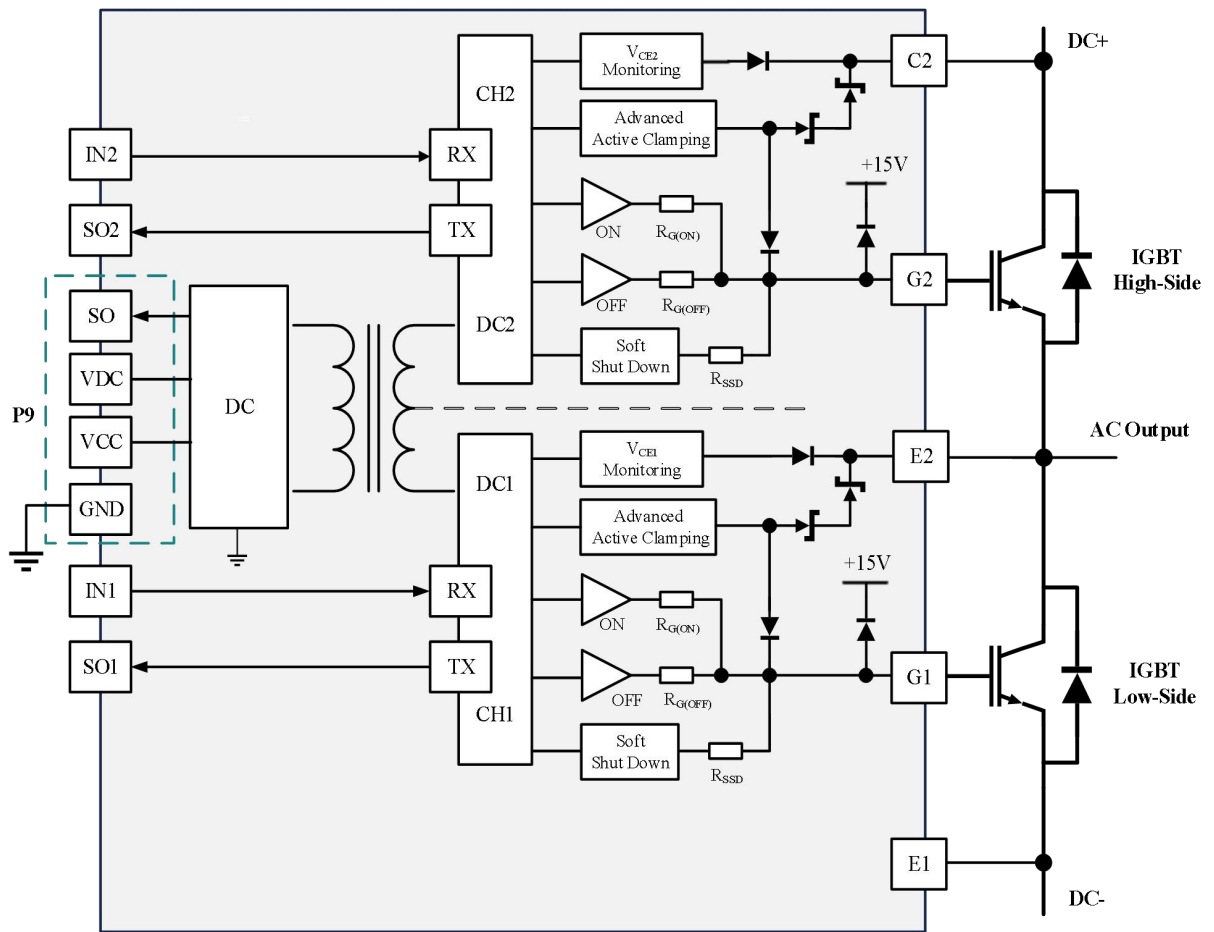
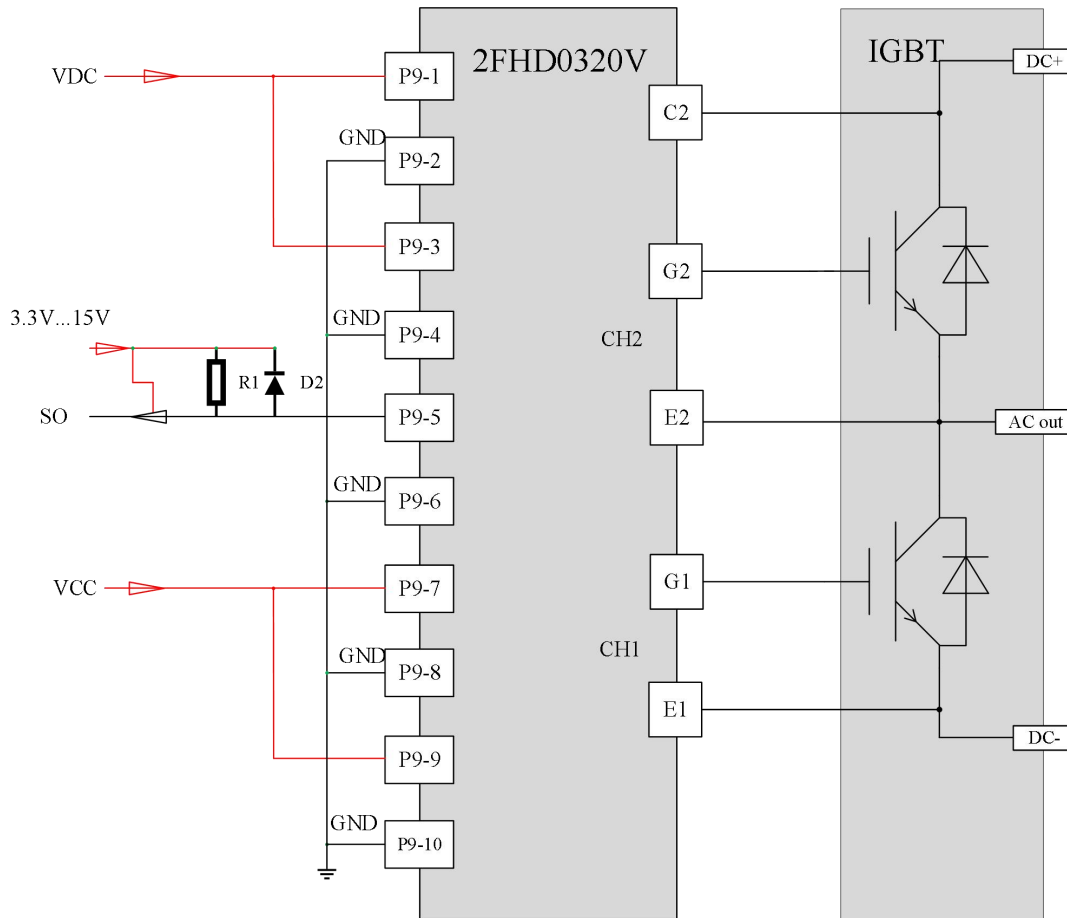


Fig.2 Functional block diagram

Connector P9 Recommended Interface Circuitry



P9 terminal pin designation

Pin	Definition	Function	Pin	Definition	Function
1	VDC	For DC/DC power supply +15V	6	GND	Primary side ground
2	GND	Primary side ground	7	VCC	For primary side power supply +15V
3	VDC	For DC/DC power supply +15V	8	GND	Primary side ground
4	GND	Primary side ground	9	VCC	For primary side power supply +15V
5	SO	Primary side fault (high normal, low fault)	10	GND	Primary side ground

Technical Parameters

Absolute Maximum Ratings

Parameter	Remarks	Min	Max	Unit
Power supply V_{DC}	V_{DC} to GND	0	15.5	V
Logic input and output voltages	Primary side, to GND	0	$V_{DC}+0.5V$	V
Output power per channel	@85°C		3	W
Gate peak current	@85°C	-20	20	A
Test voltage(50Hz/1min)	Primary to secondary side	5000		V_{RMS}
Maximum DC bus voltage	2FHD0320		1300	V
Operating temperature		-40	85	°C
Storage temperature		-40	90	°C

Recommended Operating Conditions

Parameter	Remarks	Min	Typ	Max	Unit
Power supply V_{DC}	V_{DC} to GND	14.5	15	15.5	V
Supply current I_{DC}	Without load		0.08		A
Coupling capacitance C_{IO}	Primary to secondary side		20		pF
Supply undervoltage threshold	Primary side		12		V

Gate Driver Parameters

Output level	Remarks	Min	Typ	Max	Unit
Gate voltage V_{GE}	Turn on (ON)	14.5	15	15.5	V
Gate voltage V_{GE}	Turn off (OFF)	-8.5	-8	-7.5	V

Short-Circuit Protection

Parameter	Remarks	Min	Typ	Max	Unit
V _{CE} monitoring threshold	Short-circuit monitoring threshold		10		V
Response time	CH1, Note 1		8.0		μs
	CH2, Note 1		8.0		μs
Soft shut down time	Soft shut down action time		4.16		μs

Timing Characteristics

Parameter	Remarks	Min	Typ	Max	Unit
Turn-on delay	Note 2		400		ns
Turn-off delay	Note 3		650		ns
Rise time	Note 4		10		ns
Fall time	Note 5		100		ns
Fault blocking time			80		ms
Fault indication time			200		μs

Electrical Isolation

Parameter	Remarks	Min	Typ	Max	Unit
Creepage distance	Primary to secondary side, Note 1	13			mm
	Secondary to secondary side, Note 1	8			mm
Clearance distance	Primary to secondary side	8			mm
	Secondary to secondary side	7			mm

Unless otherwise specified, all data are based on tests at +25°C ambient temperature and V_{DC}=15V.

Note:

1. Response time: the time from the occurrence of the fault to the start of soft shut down;
2. Turn-on delay: the time required to transmit from the rising edge of the PWM signal from the primary input to the rising edge of the secondary of the gate driver;
3. Turn-off delay: the time required to transmit from the falling edge of the PWM signal from the primary input to the falling edge of the secondary side of the gate driver;
4. Rise time: the amount of time from 10% of the gate turn-off voltage (-8V) to 90% of the gate turn-on voltage (+15V);
5. Fall time: the amount of time from 90% of the gate turn-on voltage (+15V) to 10% of the gate turn-off voltage (-8V);
6. Creepage distance: refer to IEC61800-5-1-2007, meet the basic isolation requirements for altitudes below 2km and pollution level 2; this value takes the creepage distance of the isolation device.

Gate Resistor & Capacitor & Fiber Optic Indication

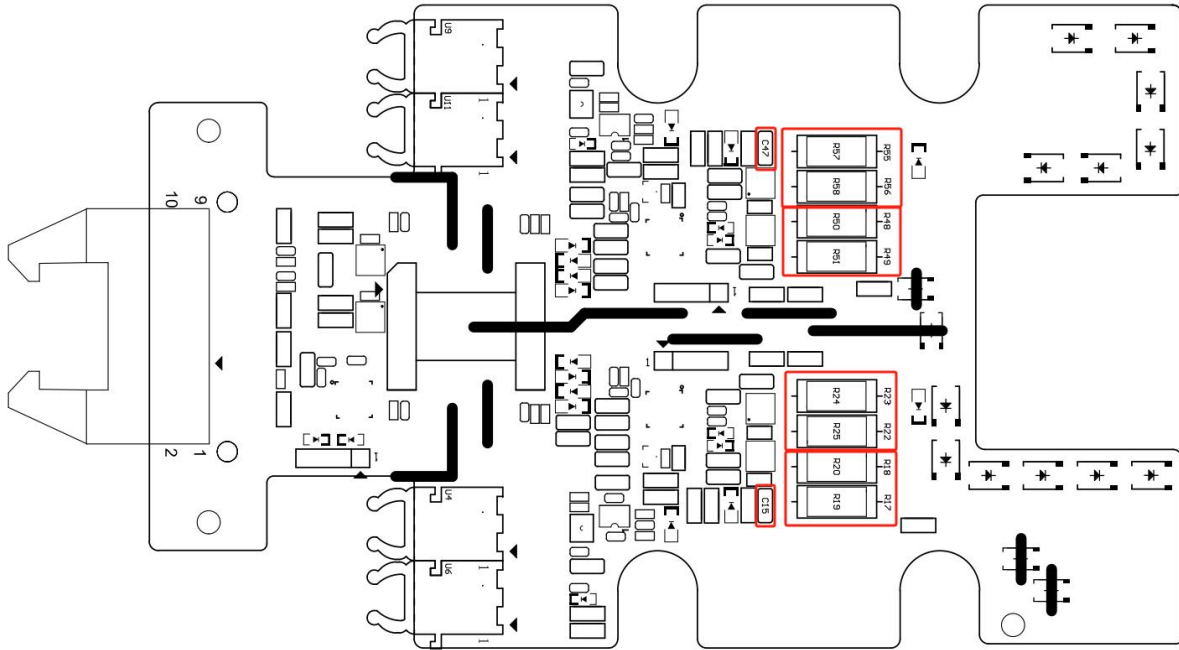


Fig. 3 Gate resistor & capacitor & fiber optic indication

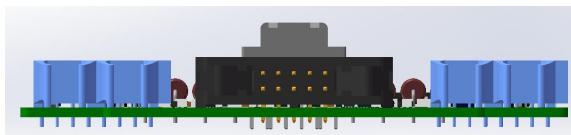
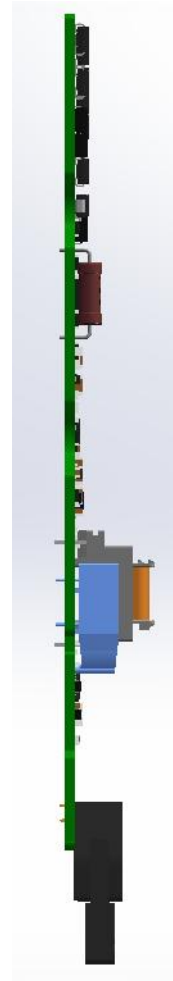
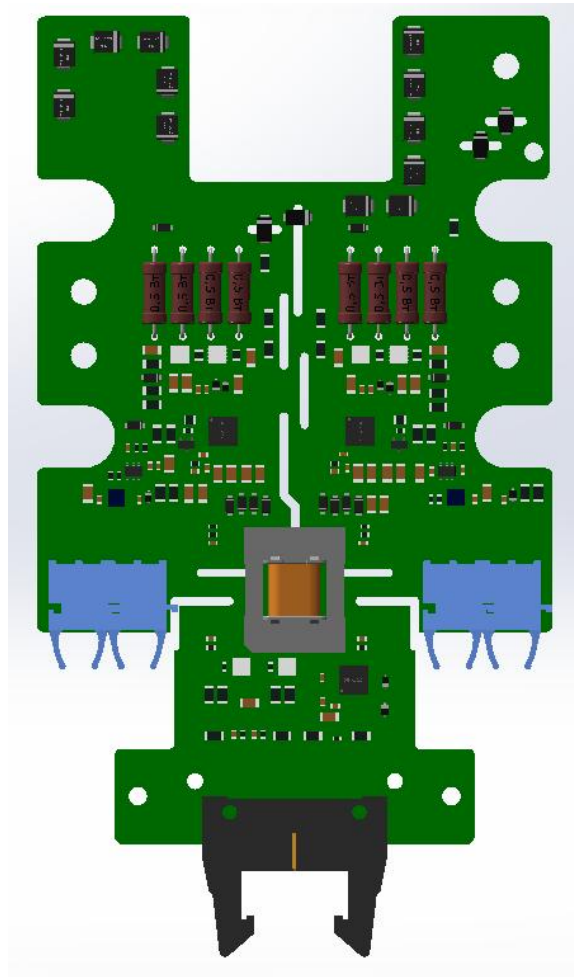
Gate resistor & capacitor calculation formula

Parameter	R _{GON}	R _{G_{OFF}}	C _{GE}
CH1	R50//R51(SMT) R48//R49(DIP)	R57//R58(SMT) R55//R56(DIP)	C47
CH2	R19//R20(SMT) R17//R18(DIP)	R24//R25(SMT) R22//R23(DIP)	C15

Recommended resistor specifications

Option	Driving power	Manufacturer	Package type	Individual resistor power	Dimension
1	<1W	YAGEO	2512SMT	1W	Length × Width: 3.2mm × 1.6mm
2	1W<P<3W	Tyohm	DIP	2W	Diameter × Length: 4.5mm × 11mm

3D and Mechanical Dimensions



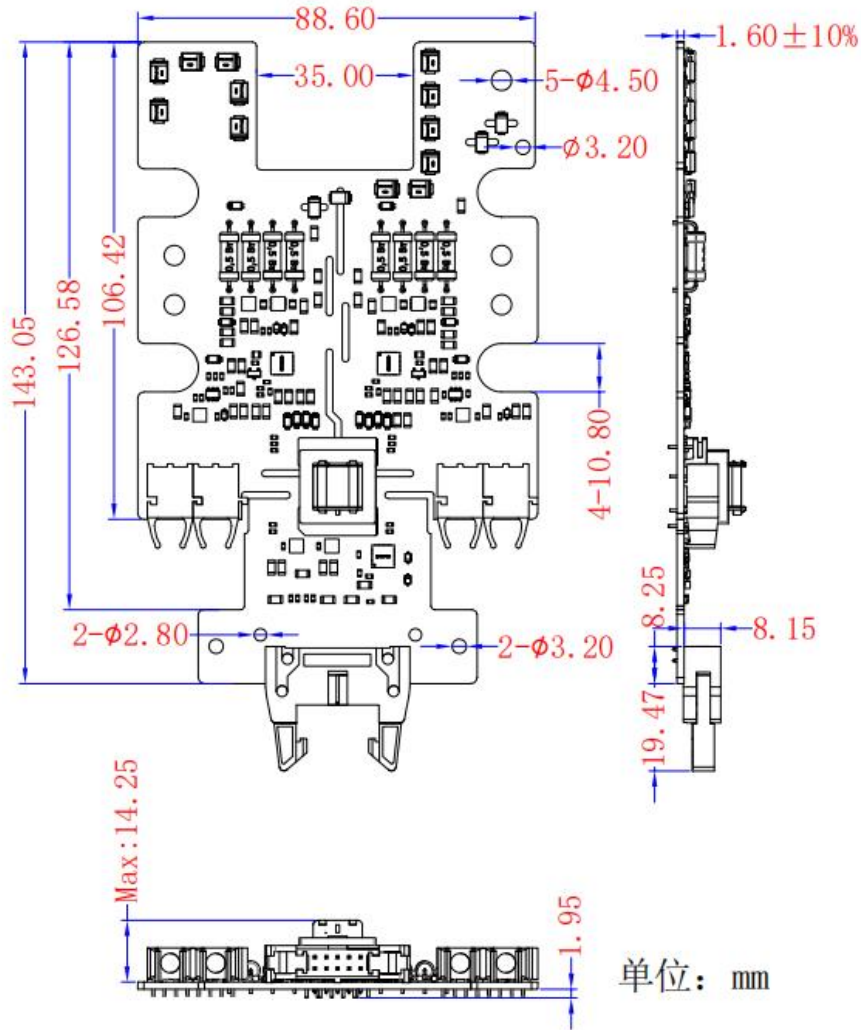


Fig. 4 3D and mechanical dimensions(unit: mm)

Note: 1.The thickness tolerance of the board is $\pm 10\%$;

2.Other dimensional tolerances refer to GB/T1804-m.

No.	Description	Manufacturer	Part number	Recommended terminal	Circuit group
P9	10Pin connector	Nextron	Z-230011810209	Z-81010100124000	/
U4, U9	Launching fiber-optic	AVAGO	HFBR2521Z	/	/
U6, U11	Receiving fiber-optic	AVAGO	HFBR1521Z	/	/

Ordering information

The 2FHD0320V can support PrimePACK™ modules from multiple manufacturers. If the products in the selection list below cannot meet your needs, please contact the Firstack sales department for customization.

Part number	Operating mode	SOx	Note
2FHD0320V17A1-Y0100	Direct	OD	1700V with TVS, Rgon=2.35, Rgoff=3.4, C _{GE} =NC, lead free
2FHD0320V17A1	Direct	OD	With TVS, NC, lead free
2FHD0320V12A1	Direct	OD	With TVS, NC, lead free
2FHD0320V17B1C-Y0001	Direct	OD	Without TVS, Rgon=1.65, Rgoff=3.4, C _{GE} =NC, lead free

Technical support

Firstack's professional team will provide you with business consultation and technical support. Please contact the Firstack technical sales team if you require the application manual for further information of the technical application.

Legal disclaimer

The instruction manual provides a detailed description of the product but does not commit to providing specific parameters regarding the delivery, performance, or applicability of the product. This document does not offer any express or implied warranties or guarantees.

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